

7-6-04

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PTO/SB/21 (02-04)

**TRANSMITTAL  
FORM**

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission	6 w/29 refs	Application Number	10/784,613
		Filing Date	February 23, 2004
		First Named Inventor	Tsivdis, Yannis
		Art Unit	2681
		Examiner Name	
		Attorney Docket Number	021673-000210US

**ENCLOSURES (Check all that apply)**

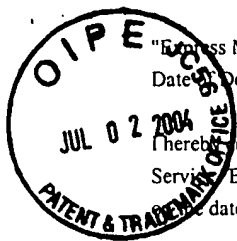
<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment/Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> IDS w/PTO/SB/08A & B (4pgs) <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): 1. <b>This PTO/SB/21 Transmittal Form (1pg)</b> 2. <b>Copies of 29 cited references</b> 3. <b>Return Postcard (1pg)</b>		
<table border="1"><tr><td>Remarks</td><td>The Commissioner is authorized to charge any additional fees to Deposit Account 20-1430.</td></tr></table>			Remarks	The Commissioner is authorized to charge any additional fees to Deposit Account 20-1430.
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**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT**

Firm or Individual name	Townsend and Townsend and Crew LLP	
	J. Matthew Zigmant	Reg. No. 44,005
Signature		
Date	July 2, 2004	

**CERTIFICATE OF MAILING**

Express Mail Label: <b>EV 338 444 165 US</b>		
I hereby certify that this correspondence is being deposited with the United States Postal Service with "Express Mail Post Office to Address" service under 37 CFR 1.10 on this date <b>July 2, 2004</b> and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450		
Typed or printed name	Daniel Miranda	
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By: \_\_\_\_\_

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

Yannis Tsividis

Application No.: 10/784,613

Filed: February 23, 2004

For: POWER DISSIPATION  
REDUCTION IN WIRELESS  
TRANSCEIVERS

Customer No. 20350

Confirmation No. 8357

Examiner:

Art Unit: 2681

INFORMATION DISCLOSURE  
STATEMENT UNDER 37 CFR §1.97 and  
§1.98

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The references cited on attached form PTO/SB/08A and PTO/SB/08B are being called to the attention of the Examiner. Copies of the references are enclosed. It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

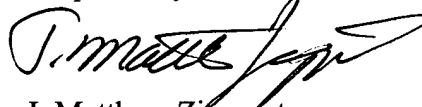
As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no

representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

Applicant believes that no fee is required for submission of this statement.

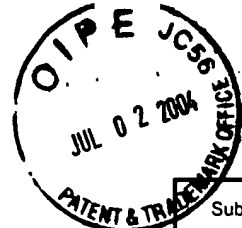
However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

Respectfully submitted,



J. Matthew Zigmant  
Reg. No. 44,005

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Two Embarcadero Center, Eighth Floor  
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JMZ:djb  
60139978 v1



<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)			<b>Complete if Known</b>		
			Application Number		
			Filing Date		
			First Named Inventor	Tsivdis, Yannis	
			Art Unit		
Examiner Name					
Sheet	1	of	2	Attorney Docket Number	021673-000210US

U.S. PATENT DOCUMENTS+					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number Kind Code <sup>2</sup> (if known)			
	AA	US-4,270,222	05-26-1981	Menant	
	AB	US-5,001,776	03-19-1991	Clark	
	AC	US-5,390,357	02-14-1995	Nobusawa	
	AD	US-5,392,457	02-21-1995	Davis et al.	
	AE	US-5,406,613	04-11-1995	Peponides et al.	
	AF	US-5,564,094	10-08-1996	Anderson et al.	
	AG	US-5,600,271	02-04-1997	Erickson et al.	
	AH	US-6,026,288	02-15-2000	Bronner	
	AI	US-6,389,445	05-14-2002	Tsivdis	
	AJ	US-6,400,177	06-04-2002	Yoshizaki	
	AK	US-6,529,069	03-04-2003	Krishnapura et al.	
	AL	US-20030112060	06-19-2003	Krishnapura et al.	
	AM	US-20030117212	06-26-2003	Krishnapura et al.	
	AN	US-6,683,492	01-27-2004	Krishnapura et al.	
	AO	US-			

FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
	AP	PCT	WO 02/35756		05-02-2002	Yoshizawa et al.		<input type="checkbox"/>
	AQ	PCT	WO 02/056558		07-18-2002	Palaskas et al.		<input type="checkbox"/>
	AR	PCT	WO 02/056470		07-18-2002	Palaskas et al.		<input type="checkbox"/>
	AS							<input type="checkbox"/>

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60139978 v1

Substitute for form 1449B/PTO			<b>Complete if Known</b>		
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)			Application Number		
			Filing Date		
			First Named Inventor		
			Art Unit		
			Examiner Name		
Sheet	2	of	2	Attorney Docket Number	021673-000210US

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T <sup>2</sup>
	AT	P.R. GRAY et al., "Future Directions in Silicon ICs for RF Personal Communications", IEEE 1995 Custom Integrated Circuits Conference		
	AU	Y. TSIVIDIS, "Minimising power dissipation in analogue signal processors through syllabic companding", Electronic Letters, Vol. 35, No. 21, October 14, 1999		
	AV	AARNO PARSSINEN et al., "A 2-GHz Wide-Band Direct Conversion Receiver for WCDMA Applications", IEEE Journal of Solid-State Circuits, Vol. 34, No. 12, December 1999		
	AW	FARBOD BEHBAHANI et al., "A Broad-Band Tunable CMOS Channel-Select Filter for a Low-IF Wireless Receiver", IEEE Journal of Solid-State Circuits, Vol. 35, No. 4, April 2000		
	AX	TUOMAS HOLLMAN et al., "A 2.7-V CMOS Dual-Mode Baseband Filter for PDC and WCDMA", IEEE Journal of Solid-State Circuits, Vol. 36, No. 7, July 2001		
	AY	FARBOD BEHBAHANI et al., "Adaptive Analog IF Signal Processor for a Wide-Band CMOS Wireless Receiver", IEEE Journal of Solid-State Circuits, Vol. 36, No. 8, August 2001		
	AZ	ATUSHI YOSHIZAWA et al., "An Anti-Blocker Structure MOSFET-C Filter for a Direct Conversion Receiver", IEEE 2001 Custom Integrated Circuits Conference		
	BA	JUSSI RYYNANEN et al., "RF Gain Control in Direct Conversion Receivers", 2002 IEEE		
	BB	ALEKSANDAR TASIC et al., "Concept of Frequency-Transconductance Tuning of Bipolar Voltage-Controlled Oscillators", 2002 IEEE		
	BC	ALEKSANDER TASIC et al., "Concept of Phase-Noise Tuning of Bipolar Voltage-Controlled Oscillators", 2002 IEEE		
	BD	Y. TSIVIDIS et al., "Internally Varying Analog Circuits Minimize Power Dissipation", IEEE Circuits & Devices Magazine, January 2003		
	BE	CHRIS VAN DEN BOS et al., "Architecture of a Reconfigurable Radio Receiver Front-End Using Overall Feedback", Electronic Research Laboratory, Delft University of Technology		

Examiner Signature		Date Considered	
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\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.